

REMARKS

Claims 2-4, 6-10, and 22-25 are pending in this application. Claims 2-4, 6-10, and 22-25 stand rejected. By this Amendment, claims 6, 22, 23, and 25 have been amended. The amendments made to claims 6, 22, 23, and 25 do not alter the scope of these claims, nor have these amendments been made to define over the prior art. Rather, the amendments to the claims have been made to improve the form thereof. In light of the amendments and remarks set forth below, Applicants respectfully submit that each of the pending claims is in immediate condition for allowance.

Claims 2-4, 6-8, 10, 12, and 23-25 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over the Admitted Prior Art in view of U.S. Patent Publication 2003/0103519 ("Balasundram"). Applicants request reconsideration and withdrawal of this rejection.

Among the limitations of claim 23 not present in the cited reference is a single second communication means where the second semiconductor chip transfers a load control data and pilot data. We note that as set forth in the specification, and recited in the claims, diagnostic data is transmitted via a first transmission channel and the load control data and pilot data are transferred via a second transmission channel. In this manner, the number of lines required for data transmission is minimized. Balasundram fails to overcome the limitations in the prior art that are noted in the present application.

Further, as set forth in the claims, a first portion of data transmitted in the frame is intended for a first semiconductor chip and a second portion of the data transmitted in this frame is intended for a second semiconductor. The Office Action asserts Balasundram discloses this feature in, for example, paragraphs 8 and 46. Paragraph 8 discloses that each transmitter receiver pair is allocated a particular time interval or channel for its data. Further, paragraph 46 discloses that the data signals are sent through a series of time intervals using multiple byte waveform technology. The multiple byte waveforms have a fixed leading byte at the start of each waveform

cycle. The first byte signifies the start of the message and the leading byte signifies the address of the message. The address is unique to the communicating transmitter and receiver pairs and ensures the receiver ignores all signals not preceded by this address.

Thus, in contrast to the assertions set forth in the Office Action, there is no teaching or disclosure in Balasundram that it would have been obvious for the plurality of data to be transferred utilizing a single allotted time interval (e.g., a single frame) as set forth in the Office Action at page 8. Thus, we respectfully submit that the claims are allowable over the combination of the Admitted Prior Art and Balasundram.

Applicants have responded to all of the rejections and objections recited in the Office Action. Reconsideration and a Notice of Allowance for all of the pending claims are therefore respectfully requested.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection of the claims and to pass this application to issue.

If the Examiner believes an interview would be of assistance, the Examiner is welcome to contact the undersigned at the number listed below.

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Respectfully submitted,

By 

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